

ESTTA Tracking number: **ESTTA498174**

Filing date: **10/04/2012**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE TRADEMARK TRIAL AND APPEAL BOARD

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| Applicant:                 | <b>Lattice Semiconductor Corporation</b> |
| Application Serial Number: | <b>85590151</b>                          |
| Application Filing Date:   | <b>04/05/2012</b>                        |
| Mark:                      | <b>LATTICE SEMICONDUCTOR</b>             |
| Date of Publication        | <b>09/04/2012</b>                        |

## **First 90 Day Request for Extension of Time to Oppose for Good Cause**

Pursuant to 37 C.F.R. Section 2.102, Micronas Semiconductor Holding AG, Technoparkstrasse 1, 8005 Zurich, , SWITZERLAND, a corporation organized under the laws of Switzerland , respectfully requests that it be granted a 90-day extension of time to file a notice of opposition against the above-identified mark for cause shown .

Potential opposer believes that good cause is established for this request by:

- The potential opposer needs additional time to confer with counsel

The time within which to file a notice of opposition is set to expire on 10/04/2012. Micronas Semiconductor Holding AG respectfully requests that the time period within which to file an opposition be extended until 01/02/2013.

Respectfully submitted,  
/rmw/  
10/04/2012

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