

TTAB



06-24-2002

U.S. Patent & TMO/c/TM Mail Rcpt Dt. #71

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Matter of Application Serial No. 76/110,451: LATTICE FROM CRYSTAL GROWTH TO FINISHED BLANKS

Published in the *Official Gazette* of March 26, 2002 Page TM 293, in International Class 9

LATTICE SEMICONDUCTOR CORPORATION, )

Potential Opposer, )

v. )

Oppn. No. \_\_\_\_\_

LATTICE MATERIALS CORPORATION, )

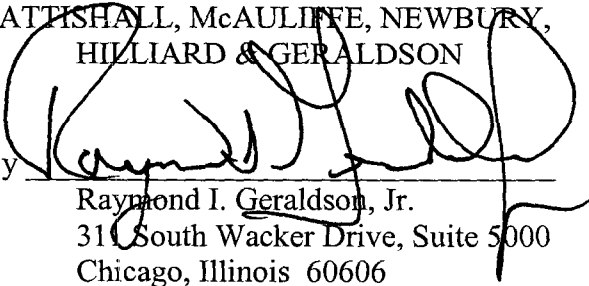
Applicant. )

TRADEMARK TRIAL AND APPEAL BOARD  
02 JUL -2 AM 8:33

**MOTION FOR EXTENSION OF TIME FOR FILING OPPOSITION**

Potential opposer, Lattice Semiconductor Corporation, hereby requests that the time for filing an opposition to the above application be extended for a period of thirty (30) days to July 24, 2002. Potential opposer's counsel and applicant's attorneys have conferred and additional time is necessary to allow potential opposer time in which to continue to analyze the pending application and determine whether opposition is to be filed. It is respectfully submitted this reason constitutes good cause within the meaning of 15 U.S.C. 1063 and Rule 2.102(c) of the Trademark Rules of Practice.

PATTISHALL, McAULIFFE, NEWBURY,  
HILLIARD & GERALDSON

By   
Raymond I. Geraldson, Jr.  
31 South Wacker Drive, Suite 5000  
Chicago, Illinois 60606  
Telephone (312) 554-8000

Attorneys for Lattice Semiconductor Corp.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Trademarks, Attn: Box TTAB, NO FEE, 2900 Crystal Drive, Arlington, Virginia 22202-3513, on June 18, 2002.

